

# FIG. 1

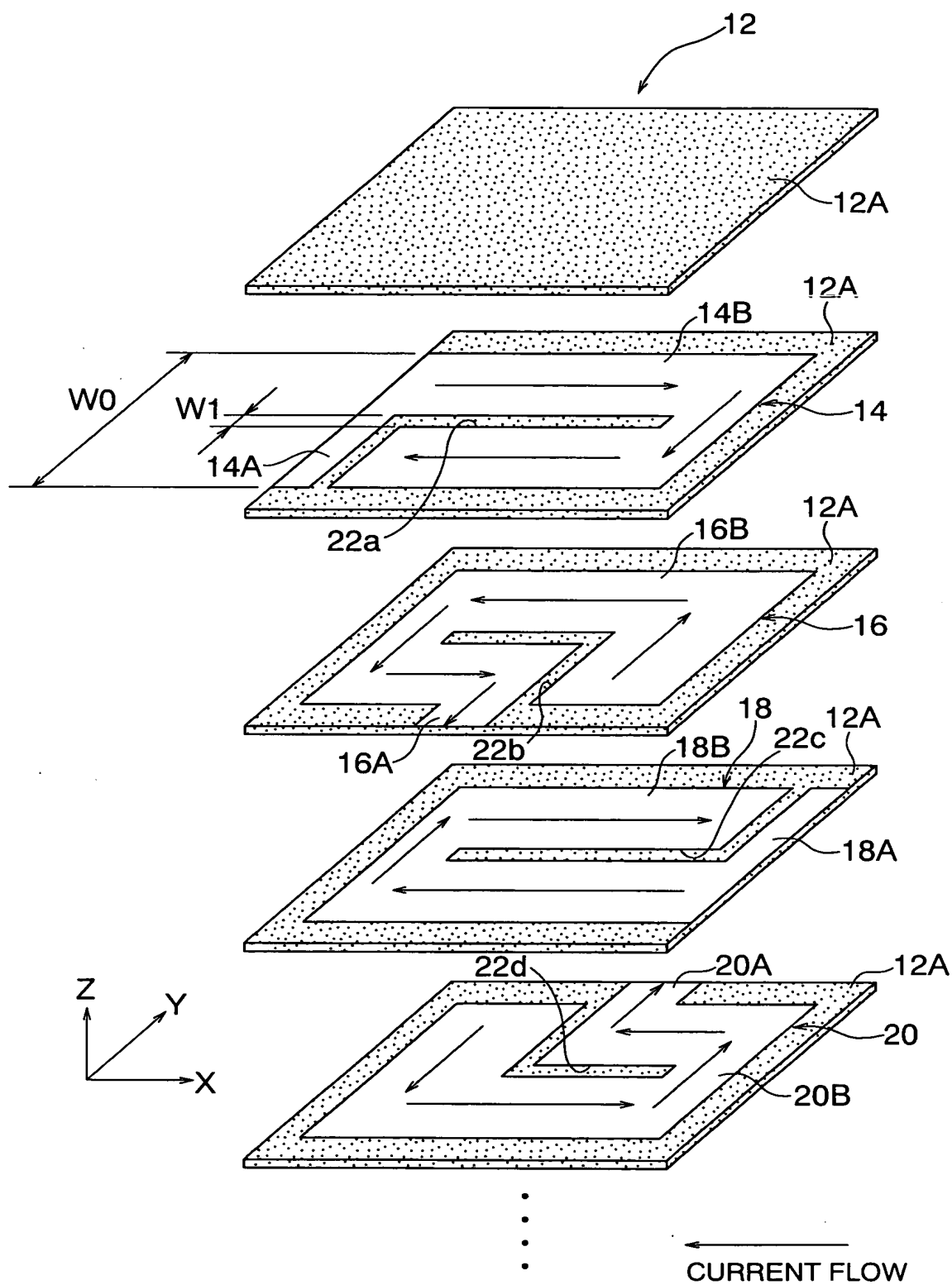


FIG. 2

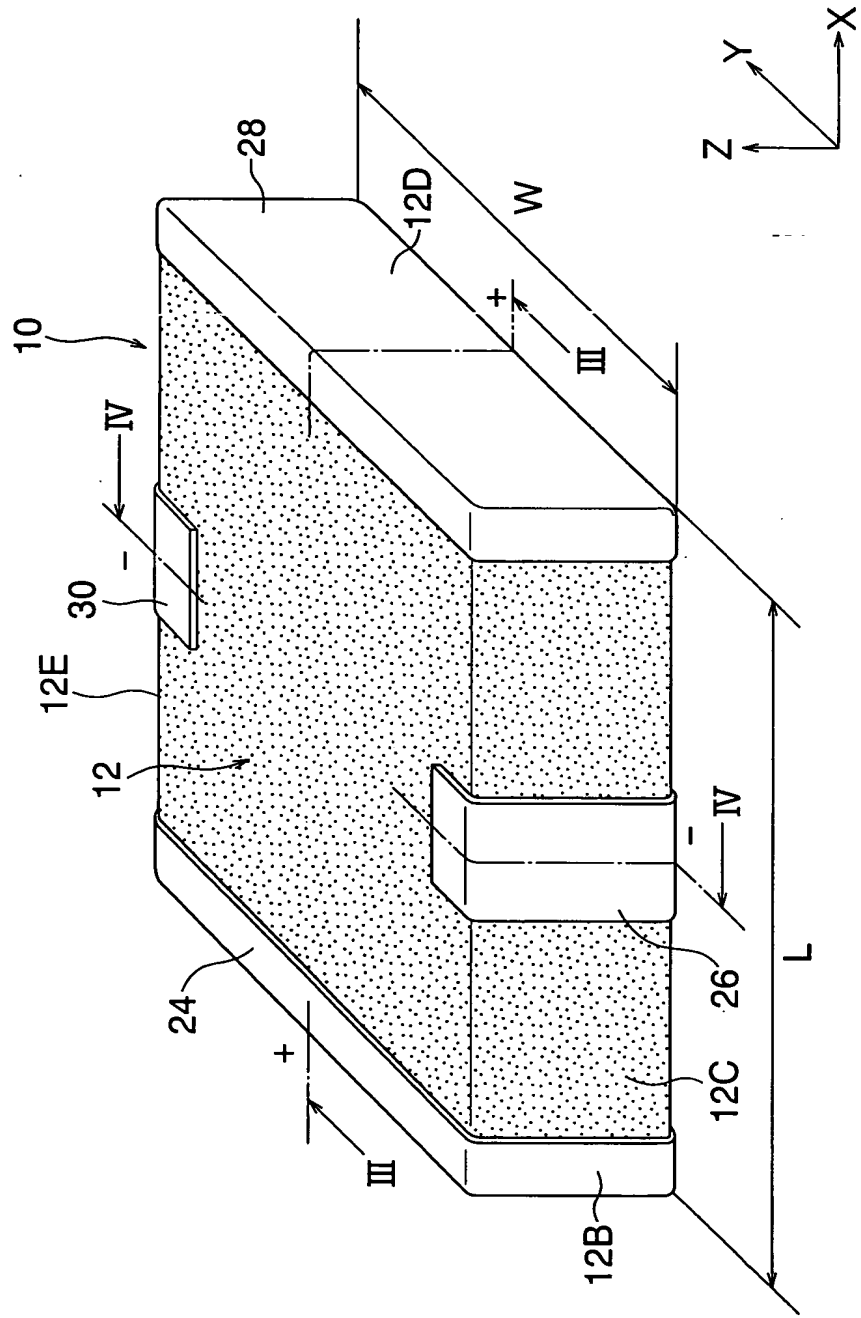


FIG. 3

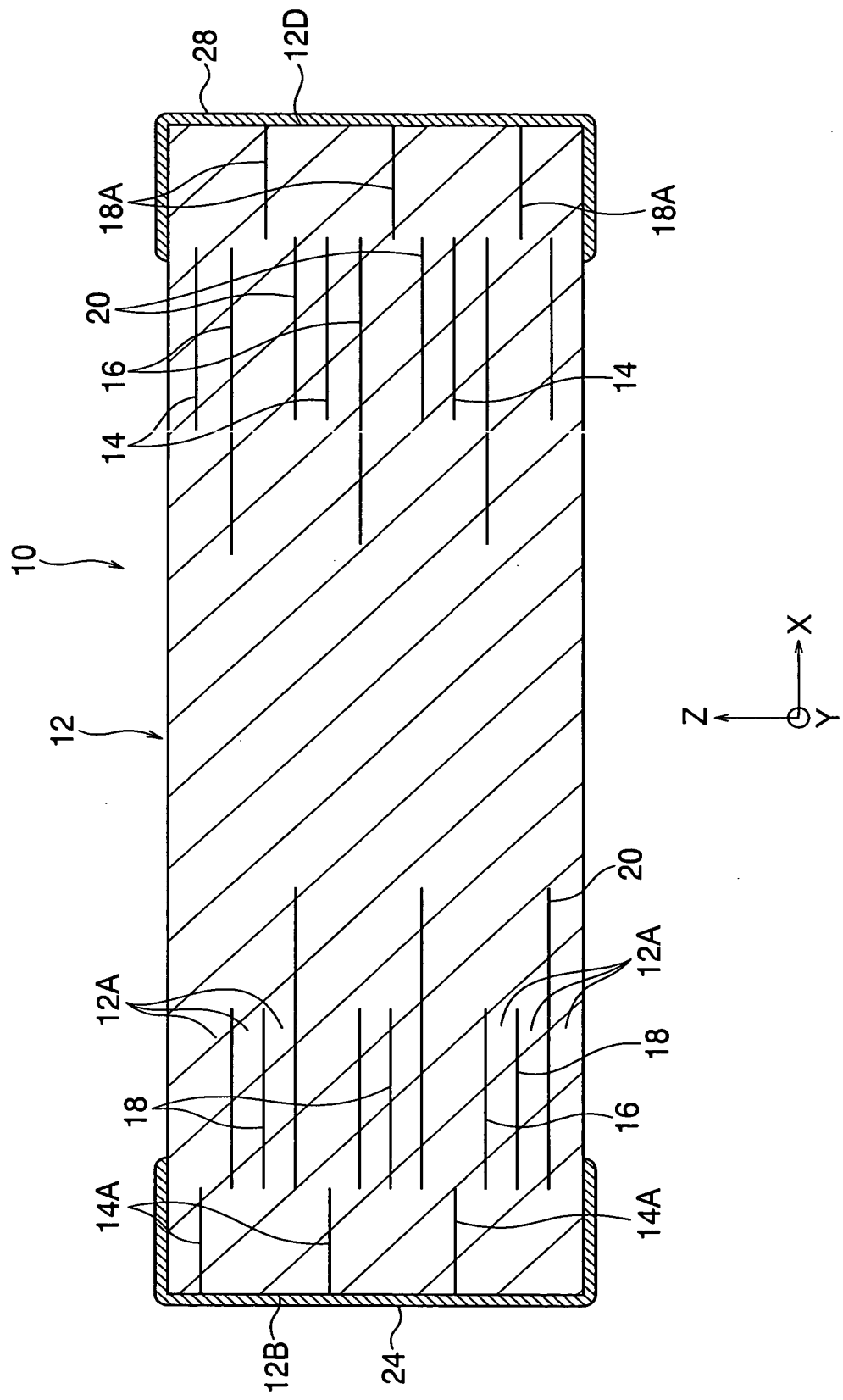


FIG. 4

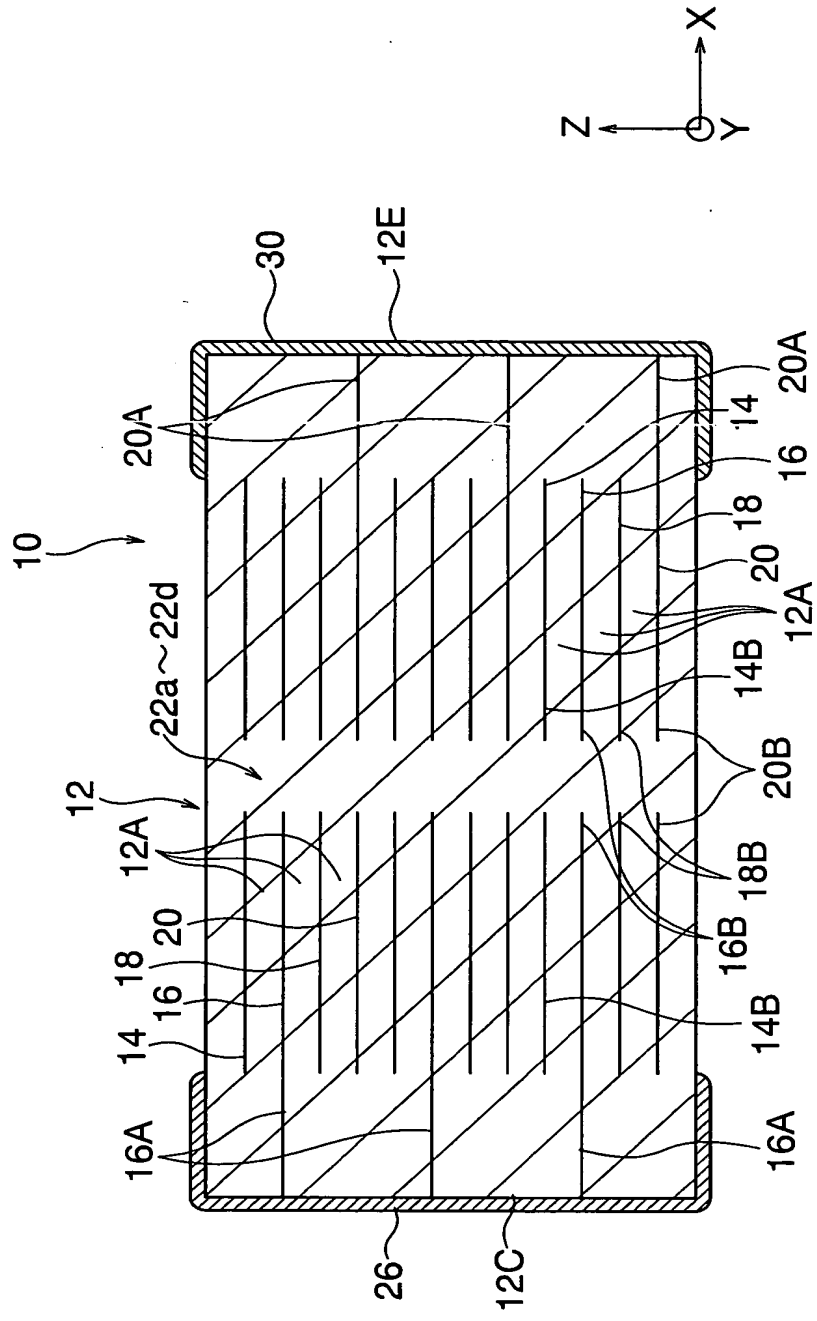


FIG. 5

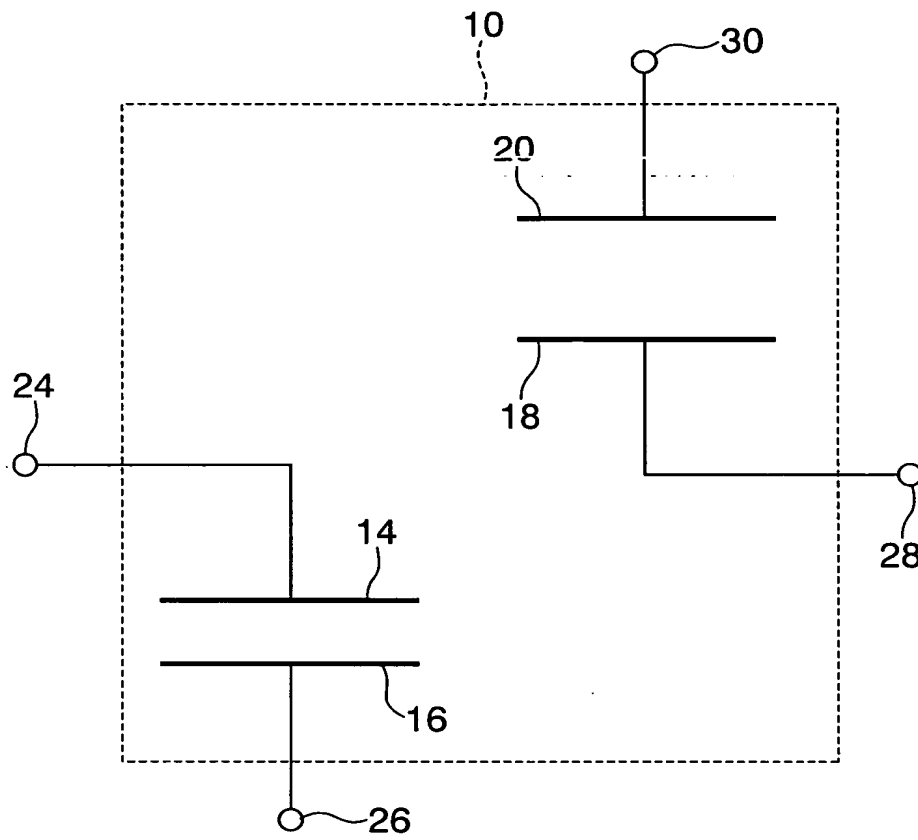


FIG. 6

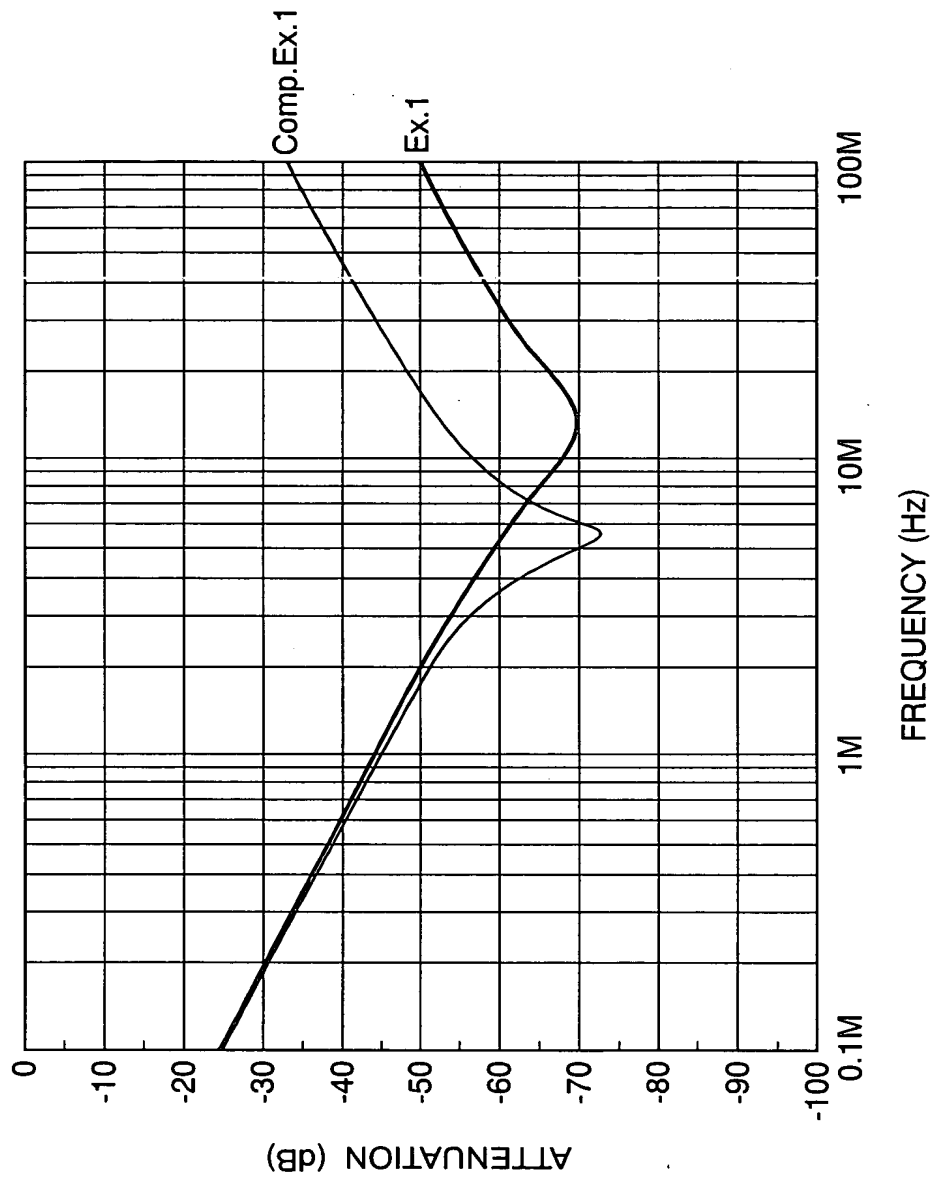
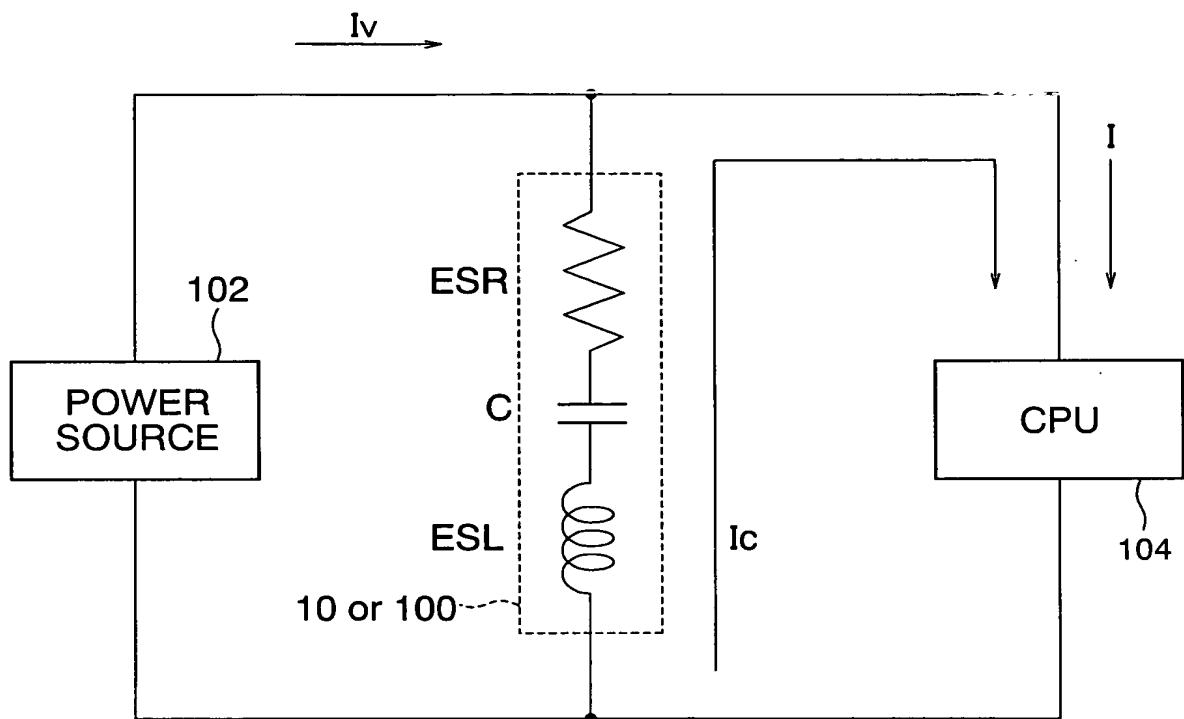
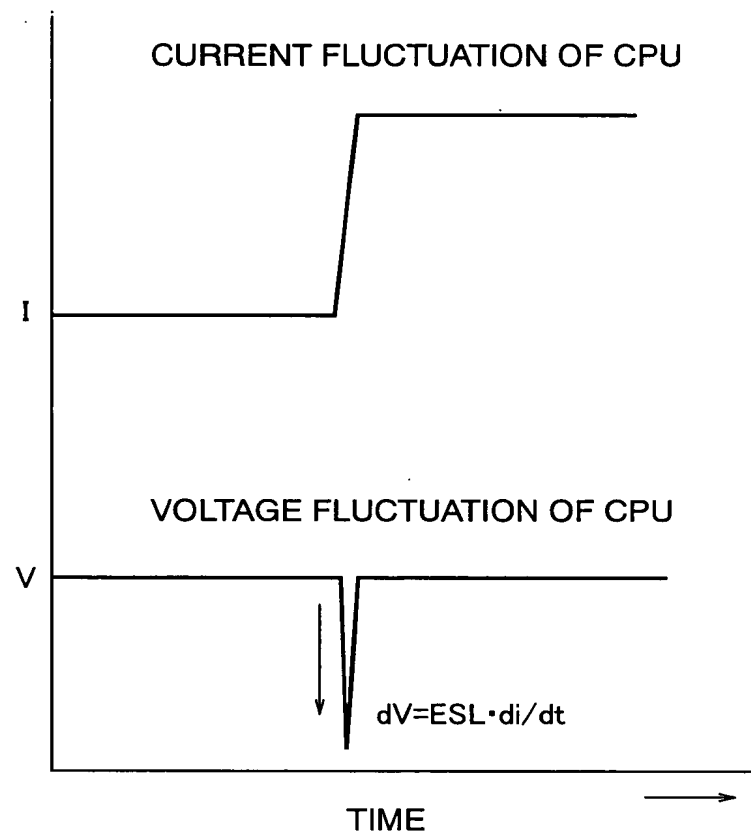


FIG. 7



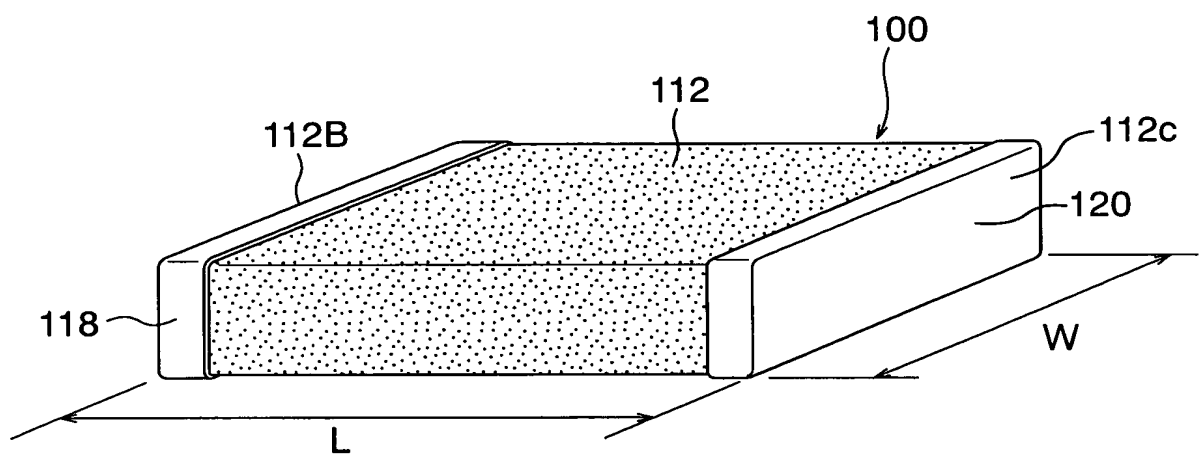
$I$ : DRIVE CURRENT OF CPU  
 $I_c$ : DISCHARGE CURRENT FROM CAPACITOR  
 $I_v$ : CURRENT FROM POWER SOURCE  
 $C$ : ELECTROSTATIC CAPACITY  
**ESR**: EQUIVALENT SERIAL RESISTANCE  
**ESL**: EQUIVALENT SERIAL INDUCTANCE

**FIG. 8**  
PRIOR ART

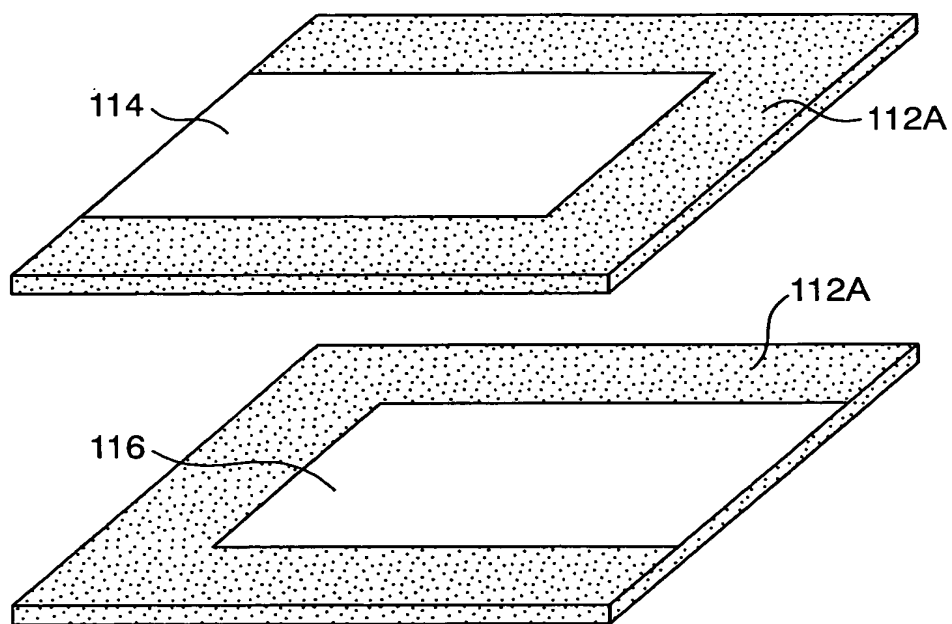




**FIG. 9**  
PRIOR ART



**FIG. 10**  
PRIOR ART



# FIG. 11

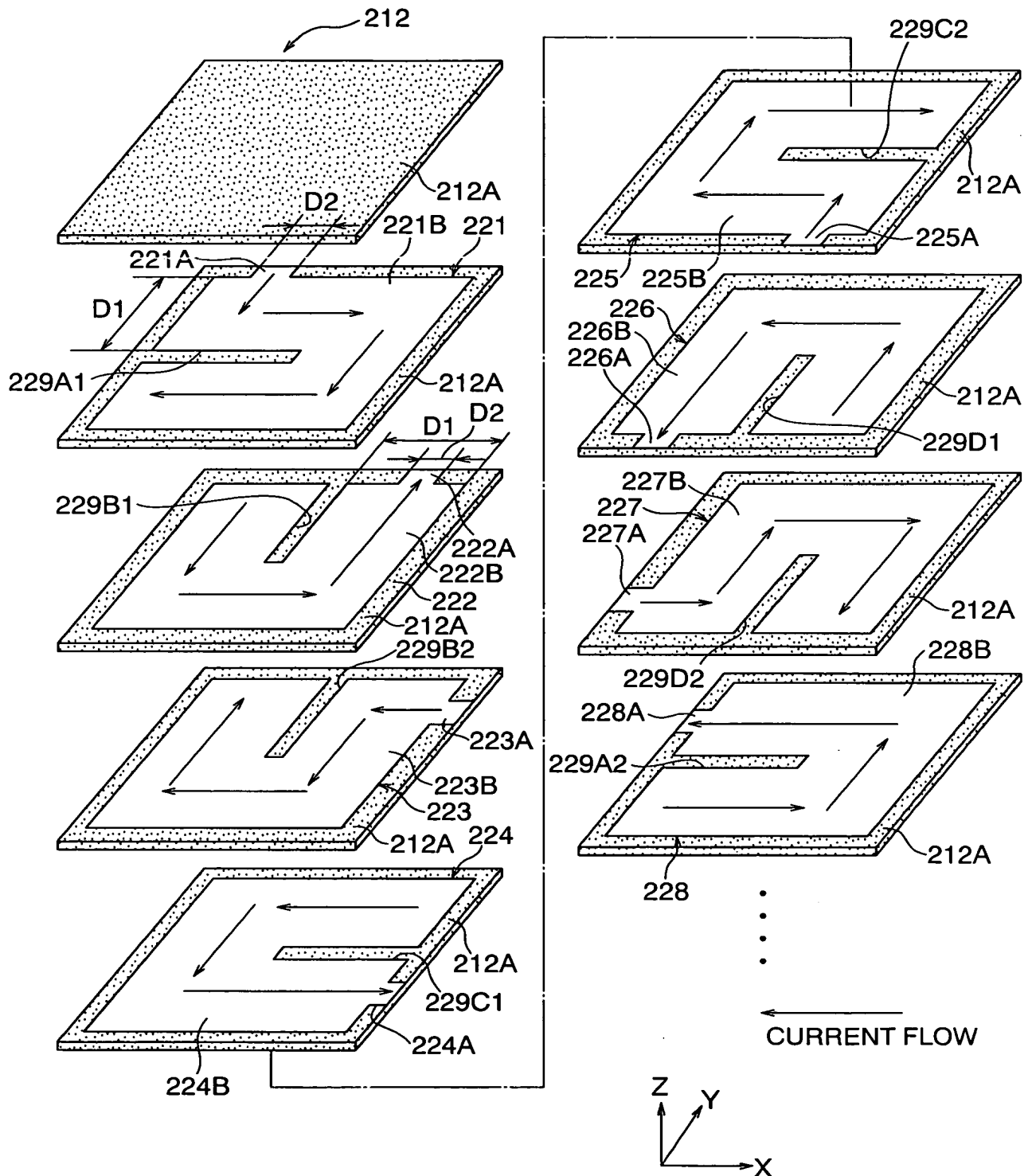


FIG. 12

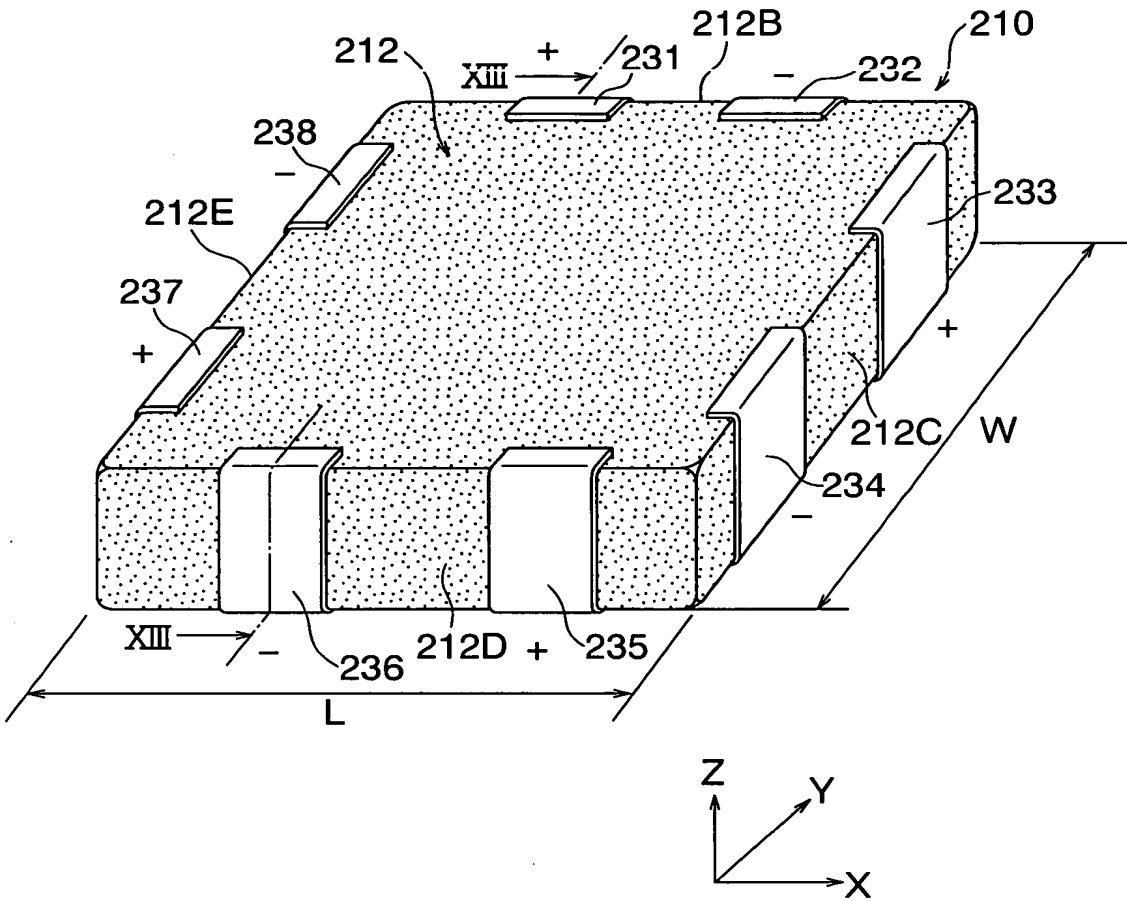


FIG. 13

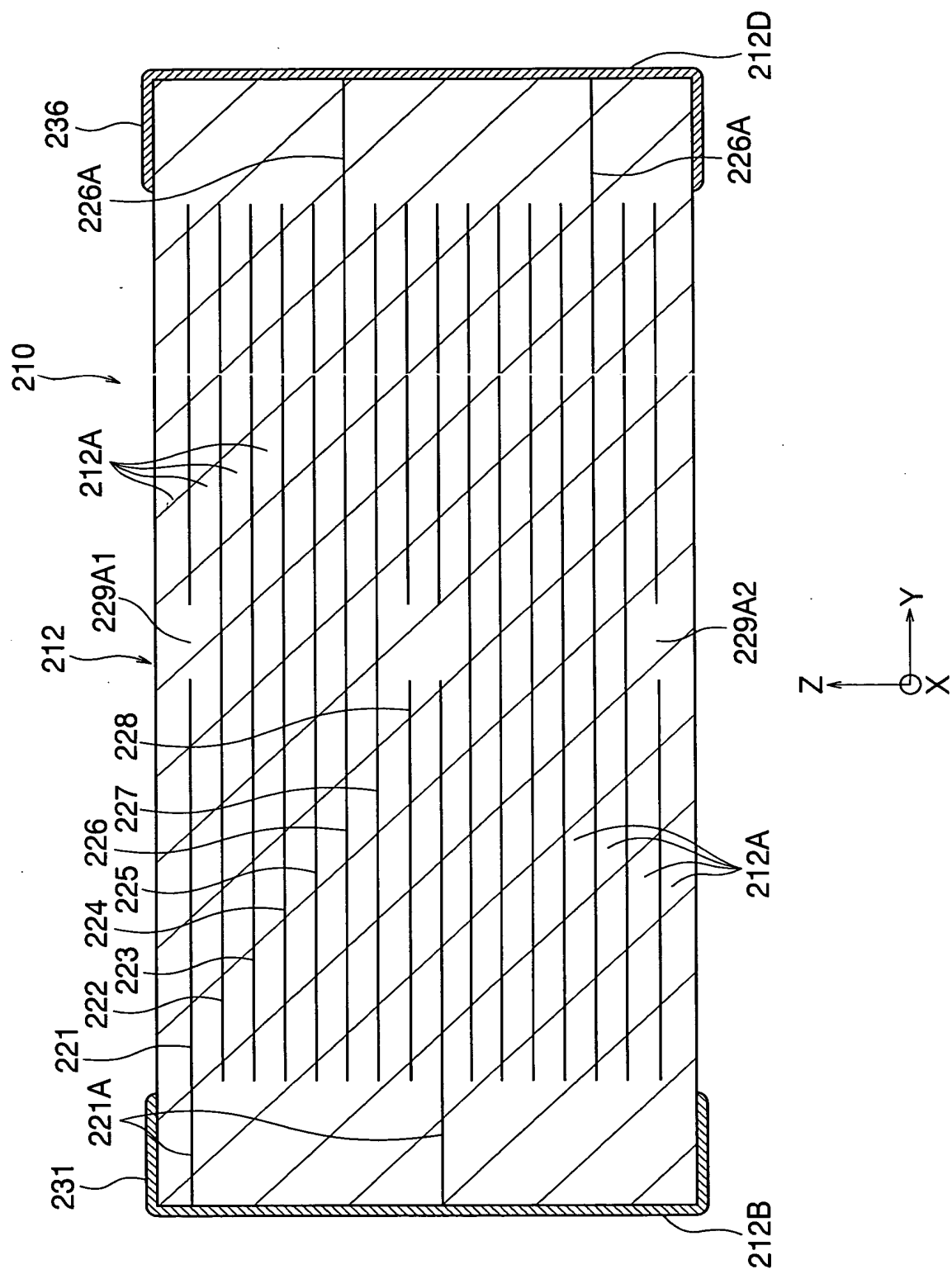


FIG. 14

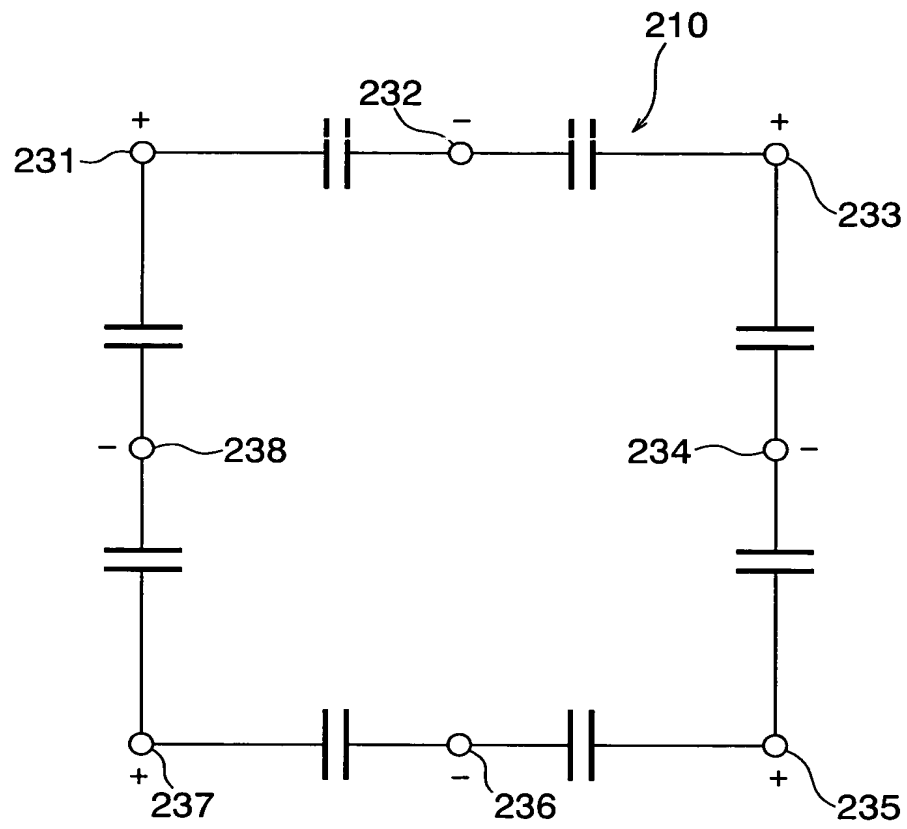
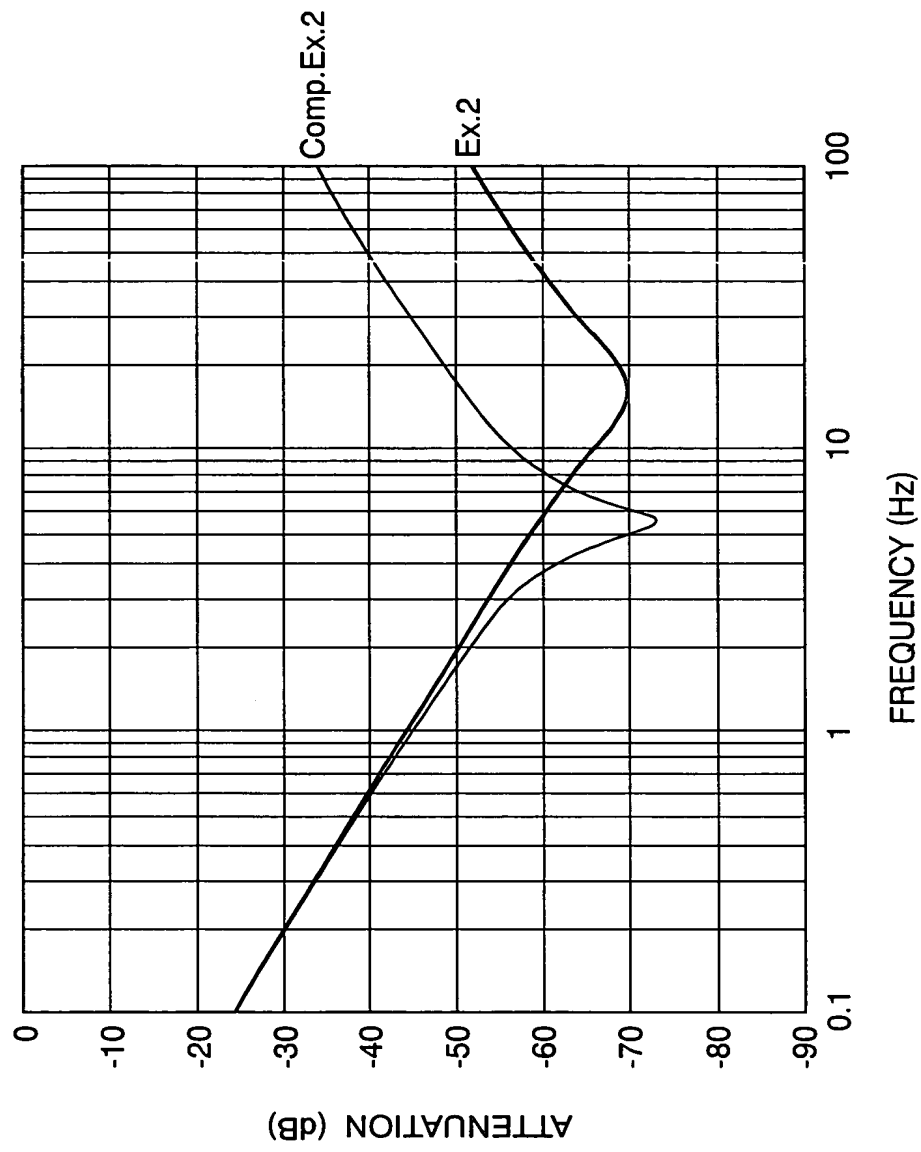


FIG. 15



# FIG. 16

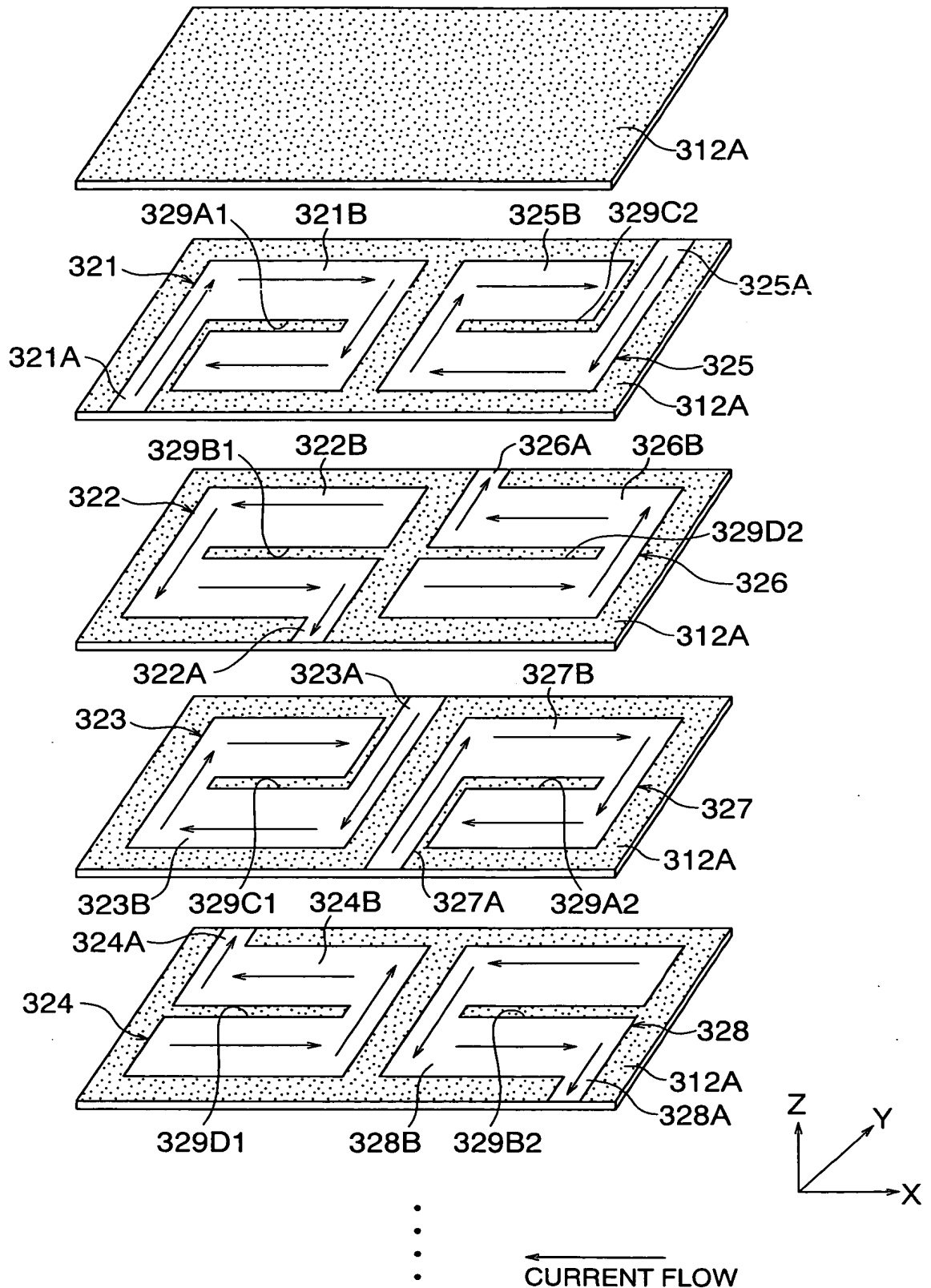




FIG. 17

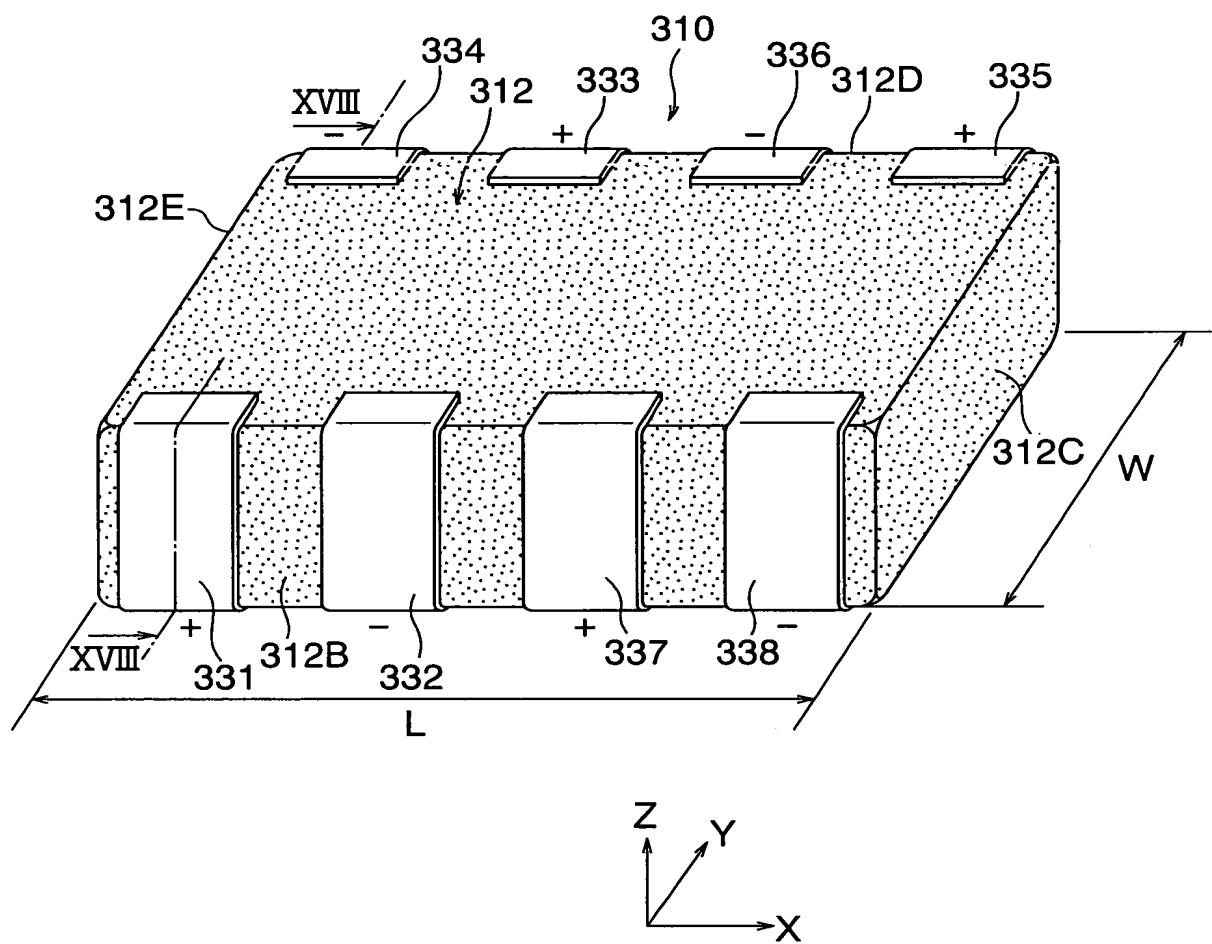




FIG. 19

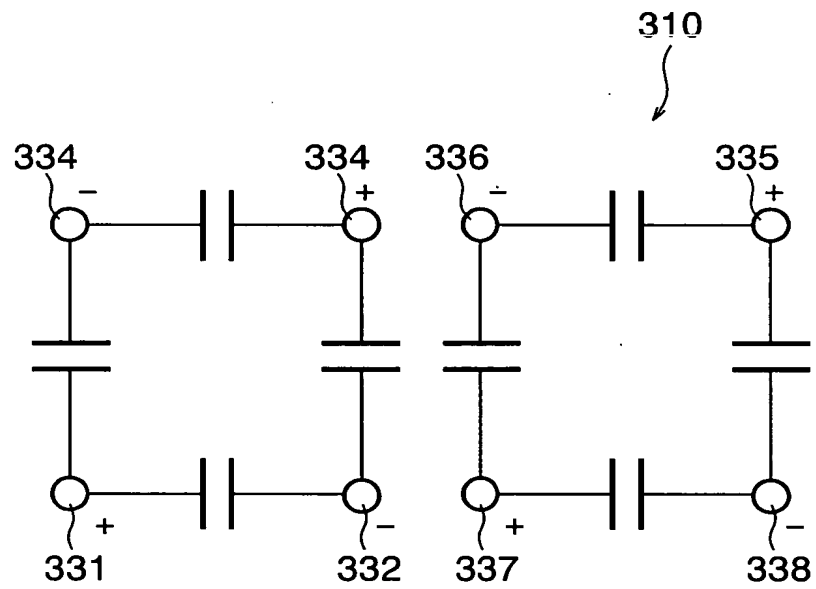


FIG. 20

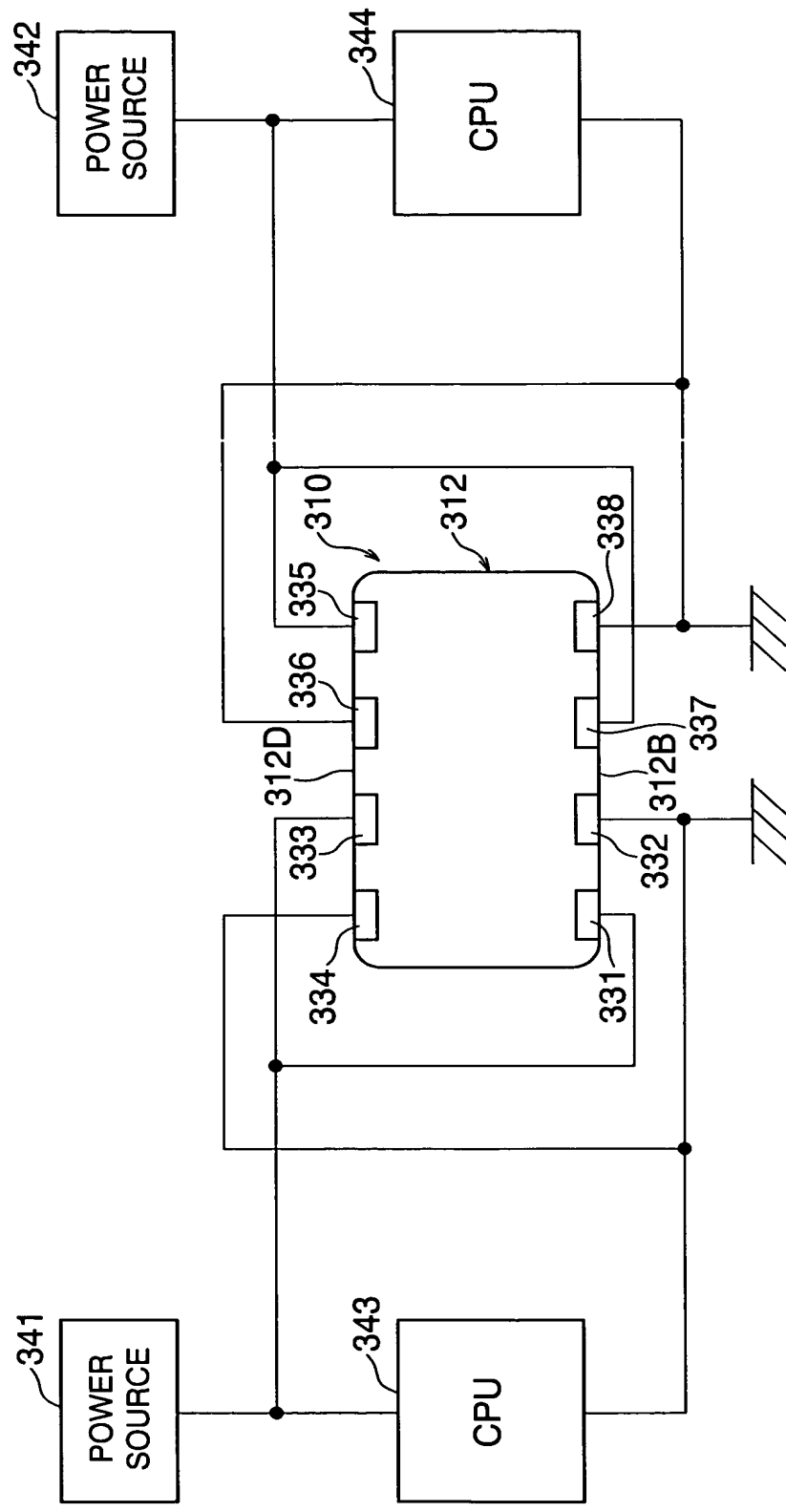


FIG. 21

